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LIST OF PUBLICATIONS  
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Attorney Docket No.  
SEL 142

Serial No.  
09/435,154

Applicant  
Shunpei YAMAZAKI et al

Filing Date  
November 8, 1999

Group  
2811

U.S. PATENT DOCUMENTS

| *EXAMINER<br>INITIAL | DOCUMENT<br>NUMBER | DATE     | NAME         | CLASS | SUB-<br>CLASS | FILING<br>DATE |
|----------------------|--------------------|----------|--------------|-------|---------------|----------------|
| Loke<br>↓<br>Loke    | 5,247,190          | 09/21/93 | Friend et al | 257   | 40            | 04/18/90       |
|                      | 5,399,502          | 03/21/95 | Friend et al | 437   | 1             | 05/05/93       |
|                      | 5,594,569          | 01/14/97 | Konuma et al | 349   | 122           | 07/20/94       |
|                      | 5,643,826          | 07/01/97 | Ohtani et al | 437   | 88            | 10/25/94       |
|                      | 5,923,962          | 07/13/99 | Ohtani et al | 438   | 150           | 04/28/95       |
|                      |                    |          |              |       |               |                |

FOREIGN PATENT DOCUMENT

|                   | DOCUMENT<br>NUMBER | DATE     | NAME                                         | CLASS | SUB-<br>CLASS | FILING<br>DATE |
|-------------------|--------------------|----------|----------------------------------------------|-------|---------------|----------------|
| Loke<br>↓<br>Loke | WO 90/13148        | 11/01/90 | Cambridge Research and<br>Innovation Ltd.    |       |               | 04/18/90       |
|                   | JP 7-130652        | 05/19/95 | Semiconductor Energy<br>Laboratory Co. Ltd.  |       |               | 10/29/93       |
|                   | JP 8-078329        | 03/22/96 | Semiconductor Energy<br>Laboratory Co. Ltd.  |       |               | 09/05/94       |
|                   | JP 10-092576       | 04/10/98 | Cambridge Display<br>Technology Ltd.         |       |               | 04/18/97       |
|                   | JP 10-135468       | 05/22/98 | Semiconductor Energy<br>Laboratory Co. Ltd.  |       |               | 10/24/96       |
|                   | JP 10-135469       | 05/22/98 | Semiconductor Energy<br>Laboratory Co. Ltd.  |       |               | 10/24/96       |
|                   | JP 10-247735       | 09/14/98 | Semiconductor Energy<br>Laboratory Co., Ltd. |       |               | 03/03/97       |
|                   |                    |          |                                              |       |               |                |

OTHER PUBLICATIONS  
(Including Author, Title, Date, Pertinent Pages)

Loke

Schenk, H. et al, "Polymers for Light Emitting Diodes," *EuroDisplay '99, Proceedings of the 19th International Display Research Conference*, September 6-9, 1999, Berlin, Germany, pp. 33-37.

Hatano, M. et al, "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT with High Reliability and Performance," *IEDM Technical Digest '97, International Electron Devices Meeting*, December 7-10, 1997, Washington D.C., pp. 523-526.

Inui, S. et al, "Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays," *J. Mater Chem.*, vol. 6, no. 4, pp. 671-673, 1996.

Yoshida, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," *SID 97 Digest*, pp. 841-844, 1997.

Furue, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," *SID 98 Digest*, pp. 782-785, 1998.

English abstract re Japanese patent application no. 7-130652, published May 19, 1995.

English abstract re Japanese patent application no. 8-078329, published March 22, 1996.

English abstract re Japanese patent application no. 10-092576, published April 10, 1998.

English abstract re Japanese patent application no. 10-135468, published May 22, 1998.

English abstract re Japanese patent application no. 10-135469, published May 22, 1998.

English abstract re Japanese patent application no. 10-247735, published September 14, 1998.

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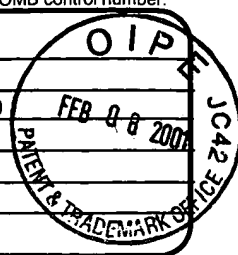
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|-------------------------------|------------------|
| <b>Application Number</b>     | 09/435,154       |
| <b>Filing Date</b>            | November 8, 1999 |
| <b>First Named Inventor</b>   | Yamazaki         |
| Group Art Unit                | 2811             |
| <b>Examiner Name</b>          | S. Loke          |
| <b>Attorney Docket Number</b> | SEL 142          |

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Examiner  
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<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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